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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey S. Mailloux et al.

Title: MEMORY DEVICE WITH PATTERNED AND PATTERNLESS ADDRESSING

Docket No.: 303.623US2

Serial No.: 08/984,560

Filed: December 3, 1997

Due Date: October 24, 2002

Examiner: Hong Kim

Group Art Unit: 2751

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Commissioner for Patents
Washington, D.C. 20231

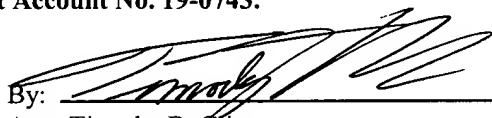
Technology Center 2100

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Response Under 1.111 (4 Pages).

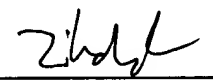
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 24 day of October, 2002.

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S/N 08/984,560

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RESPONSE UNDER 37 CFR § 1.111

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REMARKS

The Applicants' representative has carefully reviewed and considered the Office Action mailed on July 24, 2002, along with the references cited therewith. No claims are amended, no claims are canceled, and no claims are added. As a result, claims 11-21 and 59-71 are now pending in this application.

Information Disclosure Statement

The Applicants respectfully request that a copy of Form 1449, listing all references that were submitted with the Supplemental Information Disclosure Statement filed on April 17, 2001, marked as being considered and initialed by the Examiner, be returned with the next official communication.

§102 Rejection of the Claims

Claims 11-21 and 59-71 were rejected under 35 USC § 102(b) as being anticipated by Manning (U.S. Patent No. 5,610,864). The M.P.E.P. requires that "[t]he identical invention must be shown in as complete detail as is contained in the ... claim." See M.P.E.P. § 2131. Because Manning does not disclose the identical invention claimed in the Application, the Applicants respectfully traverse this rejection.

More particularly, the Applicants' representative was unable to find where Manning discloses subject matter involving "switching circuitry for switching between a first pathway and a second pathway depending on which of said patternless addressing scheme and said patterned